

Control of the state of the sta

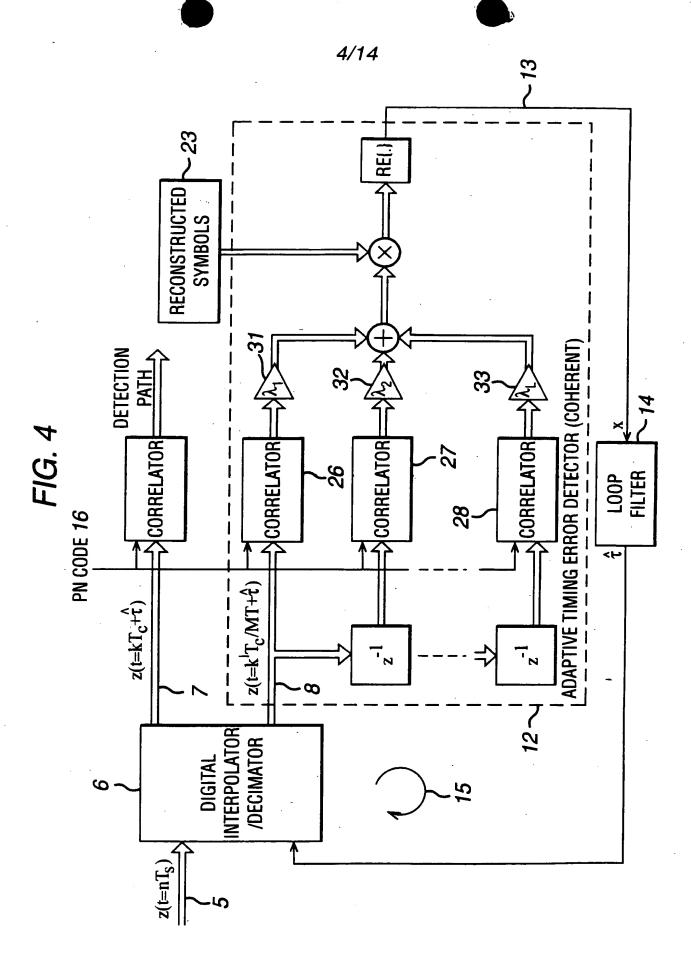
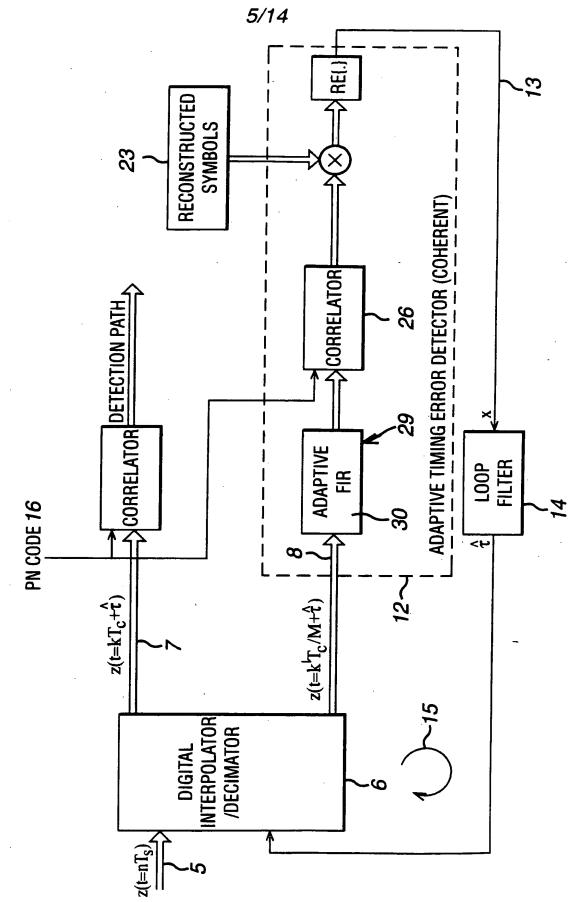
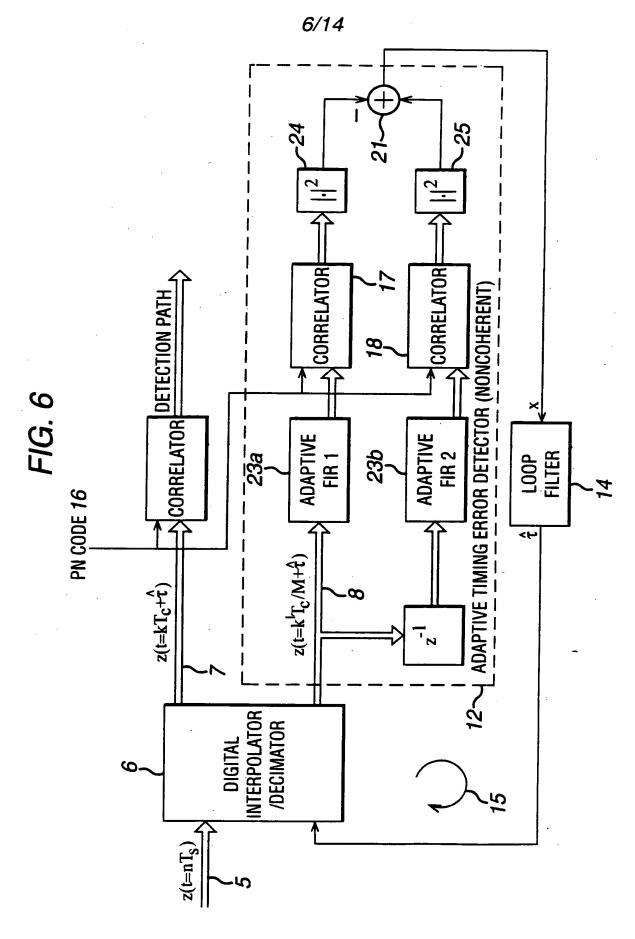
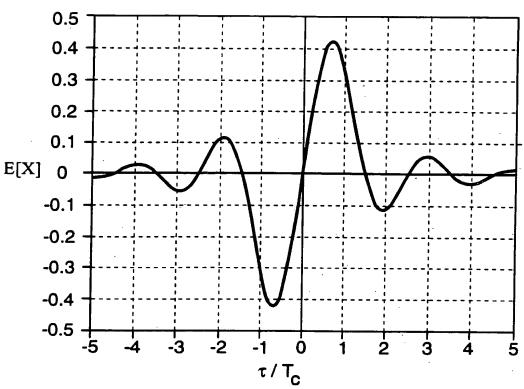


FIG. 5

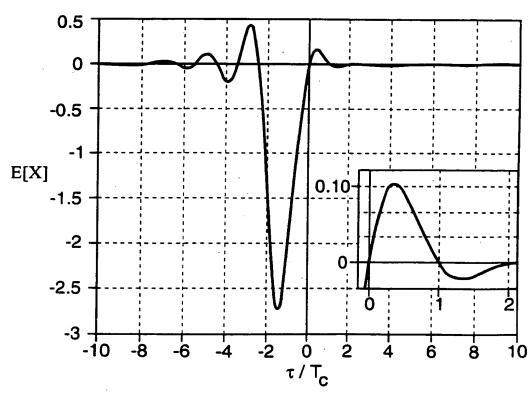








## FIG. 8



8/14

FIG. 9

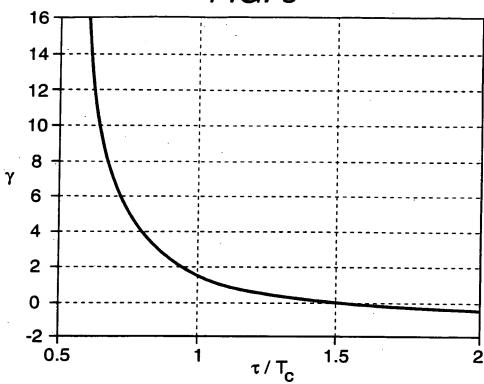


FIG. 10

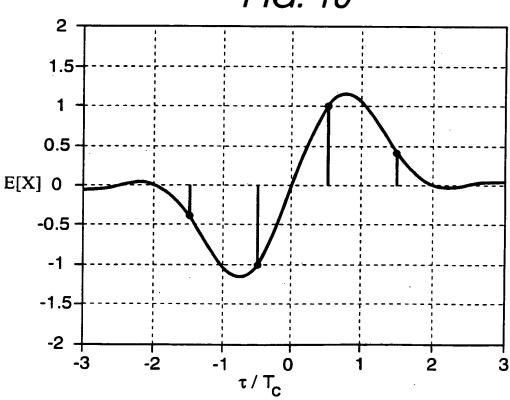
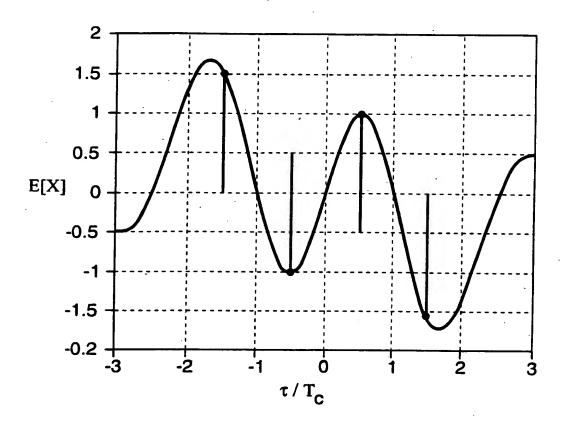


FIG. 11



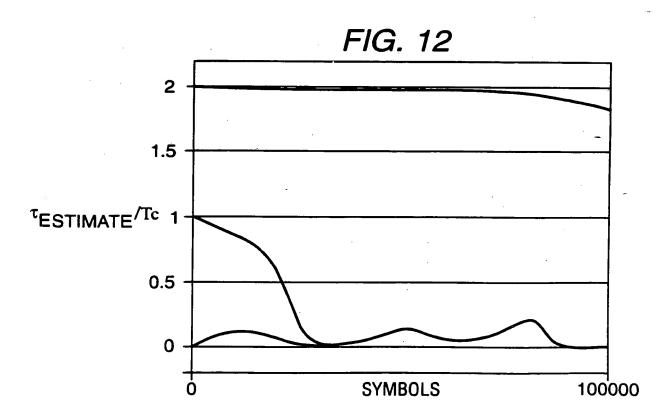


FIG. 13

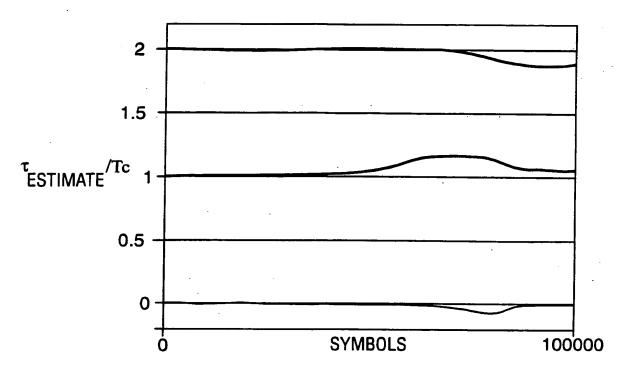


FIG. 14

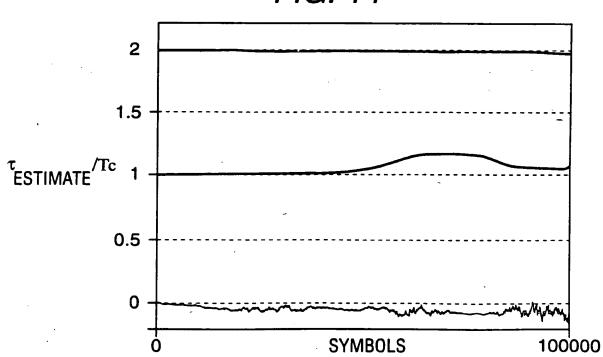
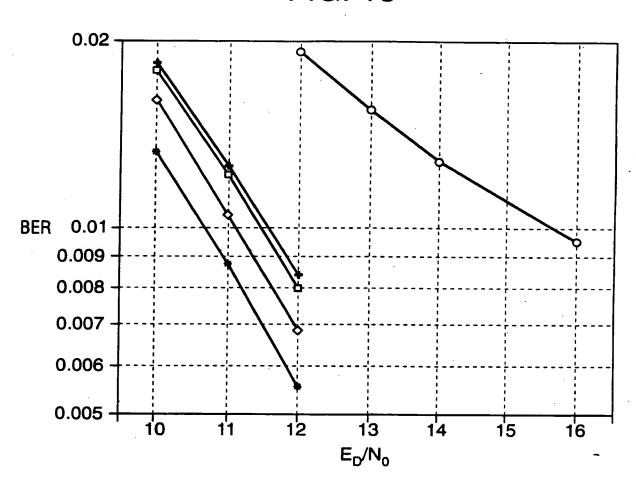
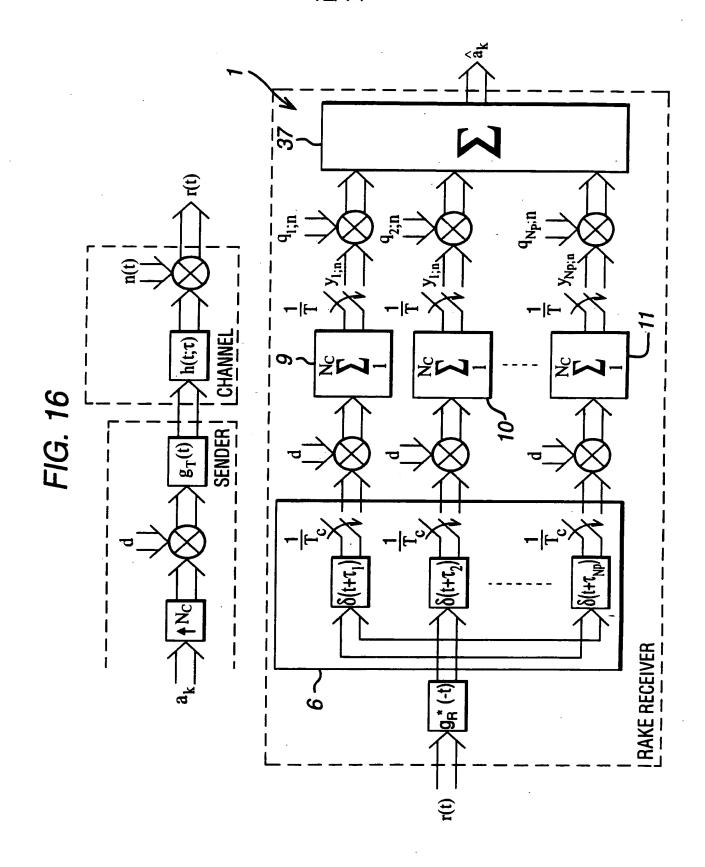


FIG. 15



- -- PERFECT SYNC
- → LMS KALMAN PHASOR EST., PERFECT TIMING
- -- MMSE TIMING
- + SIMPLE TIMING
- **-** CONVENIONAL TED



→ TO DETECTION PATH 回回 z 二 文 LOOP FILTER Z|Z FIG. 17 91 p  $1/T_{c}$  $1/T_c$ DEMUX INTERPOLATOR 2/TC z(t = kT/2)  $\frac{6}{3}$ 

13/14

